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Docket No.: 102323-0130

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Jonathan E. Greene

Dated: Mul 19, 2005 Signature

Application No.: 10/643,164

Filed: August 18, 2003

For: METHODS AND APPARATUS FOR FAST

FOURIER TRANSFORMS

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Art Unit: 2124

Examiner: C. C. Do

DECLARATION OF JONATHAN E. GREENE

I, Jonathan E. Greene, a resident of Great Barrington, Massachusetts, declare and state as follows:

- I am the inventor of the above-referenced U.S. Patent Application (the Application).
- 2. The Application generally is directed to methods of performing Fast Fourier transforms and more specifically, by way of non-limiting example, to methods of performing Fast Fourier transforms with bit reversal performed within a final stage of butterfly calculations.
- I understand that the Examiner Do has requested this affidavit to establish, for the record, that the currently pending claims are supported by U.S. Provisional Application No. 60/168,027 (the Provisional Application), filed November 30, 1999. A copy of that application is attached as Exhibit I hereto.
- 4. The currently pending claims are reprinted in the table below and, specifically, are

reprinted in the left-hand column. The right-hand columns show the start page/line and end page/line for exemplary passages in the Provisional Application that support the claims.

Claims Support Table

		Corres	ponding text from Provisional Application
Claim		Start	Line Beginning
		End Page	Line Buding
7.	A system for performing a fast Fourier	13	while (bflyent) {
ļ:	ransform on N ordered inputs in n stages	29	/* end butterfly loop */
- K	comprising: a non-final stage calculating means for	13	wp0 = SETUP->twidp;
ľ	repetitively performing in-place butterfly		· · · · · · · · · · · · · · · · · · ·
L	relculations for n-1 stages:	22	/* end penultimate pass */
┰,	a final stage calculating means for performing	22	Cri = (float *)((char *)Cr + N);
Ŀ	a final stage of butterfly calculations including:	29	/# end butterfly loop */
	a first loop means for performing a portion of the final stage butterfly calculations, the first loop means performing the set of butterfly	22	while (sent) {
ļ	calculations, and storing butterfly calculation outputs in shuffled order in place of the selected inputs to result in a correct ordering of transform outputs; and	26	/* end butterfly loop */
	a second loop means for performing a remaining portion of the final stage butterfly calculations, the second loop means performing two sets of butterfly calculations, and storing butterfly calculation outputs from a first one of the two sets of butterfly calculations in shuffled order in place of the		þflyent ≠ index >> 4;
I	inputs selected for a second one of the two sets of butterfly calculations and storing butterfly calculation outputs from the second one of the two sets of butterfly calculations in shuffled order in place of the inputs selected for the firs one of the two sets of butterfly calculations to result in a correct ordering of transform outputs.	29	/*end butterfly loop */
48.	The system of claim 47, wherein the final stage	² 2	Cr = (float *)((char *)Cr + N);
	calculations as radix-4 butterflies having four	29	
49.	The system of claim 48, wherein N is a power	13	N = 1 << LOG2N
	of two.	13	N = 1 << LOG2N
50.	The system of claim 49, wherein the non-final stage calculating means performs a first stage	13	
	of radix-8 butterfly calculations followed by m-2 stages of radix-4 butterfly calculations.	15	/* and radix-8 first pass */
51.	The system of claim 48, wherein the non-final and final stage calculating means include a		else {
	four-fold SIMD processor for performing four radix-4 butterfly calculations at a time.	29	/* end butterfly loop */

 	A method for performing a fast Fourier transform on N ordered inputs in n stages comprising:	13	while (bflycnt) {
		29	/* end butterfly loop */
	performing non-final stage calculations by repetitively performing in-place butterfly calculations for n-1 stages;	13	wp0 = SETUP->twidp;
		22	/* and penultimate pass */
	performing final stage calculations by performing a final stage of butterfly calculations in a first loop for performing a portion of the final stage butterfly calculations and in a second loop for performing a remaining portion of the final stage butterfly calculations:	22	Crl = (float *)((char *)Cr + N);
		29	/*end butterfly loop */
	wherein each of the butterfly calculations in the first loop and the second loop includes storing butterfly calculation outputs in shuffled order in place of selected inputs to result in a correct ordering of transform outputs.	22	Ct1 = (float *)((char *)Cr + N);
		29	/* end butterfly loop */
53.	The method of claim 52, wherein the final stage butterfly calculations are all performed as radix-4 butterflies having four inputs and four outputs.	22	Cr1 = (float *)((char *)Cr + N);
		29	/* end butterfly loop */
54.	The method of claim 53, further comprising storing twiddle factors for application in the butterfly calculations in groups of four, each group having an index and the groups being stored in bit reversed order based on the index.	9	n = 1 << LOG2N
		9	SETUP->twidp = twidp;

- As reflected in the table, above, the currently pending claims are fully supported in the Provisional Application
- 7. I hereby declare that all statements made herein off my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Respectfully submitted,

Date

5/17/2005

Ionathan E. Greene